



2019 FCMN Program

Monday, April 1

Reception and Registration

6:30 PM – 8:30 PM

Ferrante's Bay View Room, 10th Floor, Monterey Marriott

Tuesday, April 2

Registration and Breakfast

7:30 AM – 8:30 AM

Conference Opening

8:45 AM

Welcome and Introduction

David Seiler, NIST, Conference Chair

Keynote Talks

Session Chair: David Seiler, NIST

9:00 AM

Beyond CMOS Computing: Prospects and Best Bets

Ian Young, Intel

9:45 AM

Frontiers in Memory Technology and Metrology Drivers

Gurtej Sandhu, Micron

10:30 AM

Coffee Break and Poster/Exhibit Viewing

11:00 AM

Impact of Quantum Information Science on the Future of Nanoelectronics

Carl Williams, NIST

AI, Machine Learning, and Hybrid Metrology

Session Chairs: Ajey Jacob, GlobalFoundries, and Markus Kuhn, Intel

11:45 AM

Machine Learning and Deep Learning Opportunities for Metrology and Process Control

Phillip Leray, Imec

12:15 PM

Lunch and Poster/Exhibit Viewing

1:45 PM

Deep Learning in Atomically Resolved Imaging: from Learning Physics to Atom by Atom Fabrication

Sergei Kalinin, Oakridge

2:15 PM

AI and Machine Learning for Advanced Semiconductor Metrology and Process Control

Shay Wolfing, Nova

Microscopy

Session Chair: Alain Diebold, CNSE, SUNY Polytechnic Institute

2:45 PM

What Does Near-line TEM Bring to the Table for the CMOS Manufacturing Industry

Paul van der Heide, IMEC

3:15 PM

Coffee Break and Poster/Exhibit Viewing

3:45 PM

When Ion or Electron Channeling meets Crystal Orientation Mapping

Anne Delobbe, Tescan Orsay

4:15 PM

Low Energy Electron Resist Exposures

Rudolf Tromp, IBM

4:45 PM

Accurate and Precise Analysis of Nanoscale Semiconductor Devices with Atomprobe Tomography: a Physicist's Dream or an Analyst's Nightmare?

Wilfried Vandervorst, Imec

5:15 – 6:15 PM

Poster Session (with Drinks and Hors d'oeuvres)

6:45 PM

Banquet at Hotel (Ferrante's Bay View Room)

Wednesday, April 3

Registration and Breakfast

7:45 AM – 8:30 AM

Magnetics/Spintronics

Session Chairs: Ajey Jacob, GlobalFoundries, and Paul van der Heide, Imec

8:30 AM

Metrology for the Manufacturing Needs of MRAM

Daniel Worledge, IBM

9:00 AM

Metrology for Topological Spintronics Materials and Devices

M. Zahid Hasan, Princeton

9:30 AM

Three-Dimensional Structural and Compositional Analysis of MTJ by STEM/EDX Tomography

Yuji Otsuka, Toray Research Center

10:00 AM

Coffee Break and Poster/Exhibit Viewing

Metrology for Patterning

Session Chair: Alain Diebold, CNSE, SUNY Polytechnic Institute

10:30 AM

Critical Dimension, Thickness, and Emerging Metrology Solutions for Three Dimensional Transistors and Multi-step Patterning

Alok Vaid, GLOBALFOUNDRIES

11:00 AM

Interlayer Edge Placement Hotspots: Quantifying and Expanding Combined CD/Overlay Process Window

John Sturtevant, Mentor Graphics

11:30 AM

Optical Critical Dimension Metrology in Memory and Logic

Andy Antonelli, Nanometrics

12:00 PM

Holistic Metrology Approaches for Improved Device Overlay and Edge Placement Error

Arie den Boef, ASML

12:30 PM

Lunch and Poster/Exhibit Viewing

7-10 nm Metrology and Defect Inspection

Session Chair: Tuyen Tran, Intel

2:00 PM

Advancement on Optical Inspection Technology for 7nm or Below Process Development and Manufacturing

Yalin Xiong, KLA-Tencor

2:30 PM

Extending Electron Beam Technology Further/Deeper into the Metrology Space for 7nm or Below Process Development and Manufacturing

Ofer Adan, AMAT

3:00 PM

Advancement on Massively Parallel Electron Beam Inspection Technology for 7nm or Below Process Development and Manufacturing

Oliver Patterson, HMI

3:30 PM

Coffee Break and Dedicated Poster/Exhibit Viewing Session

Nanoscale and Interfacial Compositional Characterization and Metrology

Session Chair: Markus Kuhn, Intel

4:00 PM

Micro X-ray Fluorescence for Integrated Dopants and Thin Film Analysis

Wenbing Yun, Sigray

4:30 PM

Integrated Process Learnings with Hybrid Characterization

Ying Zhou, Intel

5:00 PM

PIFM Nanoscale Chemical Probe for Novel Patterning Applications

Sung Park, Molecular Vista

5:30 – 6:30 PM

Poster Session (with Drinks and Hors d'oeuvres)

Thursday, April 4

Registration and Breakfast

8:00 AM – 8:30 AM

Metrology for Advanced Packaging

Session Chair: Ehrenfried Zschech, Fraunhofer IKTS Dresden

8:30 AM

Existing and Future Characterization Needs

Christian Schmidt, NVIDIA

9:00 AM

Really Nondestructive High-resolution X-ray Tomography for Advanced Packaging Applications

Ehrenfried Zschech, Fraunhofer IKTS Dresden

9:30 AM

Nondestructive and Economical Dimensional Metrology of Deep Structures

Ravikiran Attota, NIST

10:00 AM

Coffee Break and Poster/Exhibit Viewing

Emerging Characterization and Metrology

Session Chairs: Usha Varsney, NSF, and Paul van der Heide, Imec

10:30 AM

Current Status and Future Possibilities of HHG Sources for Characterization/Metrology in the Semiconductor Industry

Brennan Peterson, KMLabs

11:00 AM

Atom Probe Tomography: Toward Improved Productivity and Correlative Analysis in the Semiconductor Industry

David Larson, Ametek

11:30 AM

Current Status and Future Prospects for SPM for Supporting the Semiconductor Industry

Peter De Wolf, Bruker

12:00 PM

Hybrid SIMS: How the Orbitrap Mass Analyzer Can Improve the Self-Focusing SIMS Concept for Advanced Semiconductor Structures

Alexis Franquet, Imec

12:30 PM

Lunch and Poster/Exhibit Viewing

General

Session Chair: Alain Diebold, CNSE, SUNY Polytechnic Institute

2:00 PM

Metrology Requirements for Next Generation of Semiconductor Devices

George Orji, NIST

TCAD

Session Chair: Michael Current, Current Scientific

2:30 PM

Electromigration Power Grid Checking – Novel Design and Reliability Metrology

Valeriy Sukharev, Siemens/Mentor Graphics

Metrology for Silicon Photonics and Sensors

Session Chair: Alex Liddle, NIST

3:00 PM

Scanning Probe Technology to Make Local, Non-Contact Measurements of Photonic Circuits

Vladimir Aksyuk, NIST

3:30 PM

TBD

4:00 PM

Conference End

Poster Presentations

001, Metrology of Semiconductor Devices Using Machine Learning and Active Shapes

John Flanagan, Hayley Johanesen, Mark Biedrzycki, Jack Hager, Justin Roller, Jason Arjavac, Dan Nelson, and Umesh Adiga

Thermo Fisher Scientific, Material and Structural Analysis, Hillsboro, OR

002, Image-based Dimensional Analysis for Semiconductor and MEMS Structures

Woo Sik Yoo, Kitaek Kang, Jung Gon Kim, and Toshikazu Ishigaki

Wafermasters, Inc., 2251 Brandini Drive, Dublin, CA

003, Simultaneous Measurement of Thermal Properties and Interface Thermal Resistance of Thin Films by Thermoreflectance Microscopy

Elie Badine^{1,2}, Mathieu Bardoux¹, Nadine Abboud², Ziad Herro², and Abdelhak Hadj Sahaout¹

¹Unité de Dynamique et Structure des Matériaux Moléculaires, Université du Littoral Côte d'Opale,

Dunkerque, FRANCE

²Laboratoire de Physique Appliquée, Université Libanaise, Faculté des Sciences, Campus Fanar, LIBAN

004, Automatic Data Acquisition and Analytical System for High Quality Metrology

S. Kawai¹, M. Nishikawa¹, K. Somehara¹, D. Maekawa¹, H. Hashiguchi¹, N. Endo¹, H. Sakurai¹, L. L. Wei², K. McIlwrath³, and H. Sawada¹

¹JEOL Ltd., Tokyo, Japan

²JIE DONG Co., Ltd., Taipei, Taiwan

³JEOL USA, Inc., MA, USA

005, Photo Thermal Acoustic Imaging for Sub-Surface Inspection and Alignment

W.D. Koek, E.J. van Zwet, M. Plissi, M. Eschen, D. Piras, P.L.M.J. van Neer, and M.J. van der Lans
TNO, Stieltjesweg 1, 2628 CK Delft, the Netherlands

006, Influence of Contacted Poly Pitch Dimension on Channel's Strain Level in 22nm UTBB FDSOI PMOSFET Technology

F. Ravoux¹, W. A. Gill¹, A. Al Ghaferi¹, I. Saadat¹, Z. Zhao², D. Utess², and D. Haramé²

¹Electrical and Computer Engineering Department, Khalifa University, Abu Dhabi, U.A.E.

²GLOBALFOUNDRIES, Dresden, Germany

007, A 331-Beam SEM

C. Riedesel, I. Müller, N. Kaufmann, A. Adolf, N. Kämmer, H. Fritz, and D. Zeidler
Carl Zeiss Microscopy GmbH, Carl-Zeiss-Straße 22, 73447 Oberkochen, Germany

008, Grazing Incidence X-Ray Diffraction Analysis of the Periodic Dislocation Network of Ge/Si Heterostructures

Yvo Barnscheidt¹, Jan Schmidt¹, and H. Jörg Osten^{1,2}

¹Institute of Electronic Materials and Devices, Leibniz University Hannover, Hannover, Germany

²Laboratory of Nano- and Quantum Engineering, Leibniz University Hannover, Hannover, Germany

009, Determination of the Dzyaloshinskii-Moriya Interaction in Magnetic Multilayers

Hans T. Nembach, Emilie Jué, Thomas J. Silva, and Justin M. Shaw

Quantum Electromagnetics Division, National Institute of Standards and Technology, Boulder, CO

010, Understanding the Role of Scanning Electron Microscope Image Noise in the Measurement of Pattern Roughness

Chris A. Mack¹, Frieda Van Roey², and Gian Francesco Lorusso²

¹Fractilia, LLC, 1605 Watchhill Rd, Austin, TX 78703, USA

²imec, Kapeldreef, Leuven, Belgium

011, Measuring Step Heights from Top-Down SEM Images

K. T. Arat¹, J. Bolten², A. C. Zonneville³, P. Kruit¹, C.W. Hagen¹

¹Delft University of Technology, Dept. of Imaging Physics, Lorentzweg 1, the Netherlands

²AMO GmbH, AMICA, Otto-Blumenthal-Str. 25, Germany

³Raith, De Dintel 27a, The Netherlands

012, Tomographic Mueller-matrix Scatterometry for Nanostructure Metrology: Principle and Opportunities

Xiuguo Chen and Shiyuan Liu

State Key Laboratory of Digital Manufacturing Equipment and Technology, Huazhong University of Science and Technology, Wuhan 430074, China

013, Focused Helium Ion Beam Nanofabrication of Superconducting Thin Films

Leila Kasaei¹, Hussein Hijazi², Mengjun Li³, Thomas Melbourne¹, Leonard C. Feldman², Torgny Gustafsson², Ke Chen¹ and X.X. Xi¹

¹Department of Physics, Temple University, Philadelphia, Pennsylvania

²Department of Physics and Astronomy, Rutgers University, Piscataway, NJ

³Department of Chemistry and Chemical Biology, Piscataway, NJ

014, Metrology For E-Beam Lithography Process Characterization

Richard J. Bojko¹, Ulrich Hofmann², Gerald G. Lopez³, and N. Shane Patrick⁴

¹GenISys, Inc. San Francisco, CA

²GenISys, GmbH. Taufkirchen, Germany

³Singh Center for Nanotechnology, Univ of Pennsylvania, Philadelphia, PA

⁴Washington Nanofabrication Facility, Univ of Washington, Seattle WA

015, 3D Metrology by FIB-SEM Tomography

Amir Avishai¹, David Pan¹, Keumsil Lee¹, Dmitry Klochkov², Jens Timo Neumann², Thomas Korb², Eugen Foca², and Alex Buxbaum²

¹Carl Zeiss SMT Inc., Pleasanton, CA

²Carl Zeiss SMT GmbH, Rudolf-Eber-Str. 2, Oberkochen, Germany

016, Substrate Effects in EUV Photoresist Patterning

Mengjun Li¹, Sylvie Rangan¹, Aaron Dangerfield², Hussein Hijazi², Danielle Hutchison⁴, May Nyman⁴, Robert A. Bartynski², Yves Chabal³, Torgny Gustafsson², Leonard C. Feldman², and Eric L. Garfunkel^{1,2}

¹Department of Chemistry and Chemical Biology

²Department of Physics and Astronomy, Rutgers University, Piscataway, NJ

³Department of Materials Science & Engineering, University of Texas at Dallas, Dallas, TX

⁴Department of Chemistry, Oregon State University, Corvallis, OR

017, Characterization of Subsurface Selective Etching of a Nanowire Test Structure using CD-SAXS

Madhulika Korde¹, R. Joseph Kline², Daniel Sunday², Cheryl Alix³, Subhadeep Kal³, Aelan Mosden³, and Alain C Diebold¹

¹SUNY Polytechnic Institute, Albany, NY

²National Institute of Standards and Technology, Gaithersburg, MD

³TEL Technology Center, America, LLC, Albany, NY

018, Multi-Modal Imaging and Analysis for Microcontamination Source Identification

Suwen Liu and Haizheng Zhang

Entegris, Inc. 129 Concord Road, Billerica, MA

019, Micro Four-Point Probe Seebeck Measurements on nm-Wide Si Fins

M.-L. Witthøft¹, S. Folkersma^{2,3}, T. A. Marangoni¹, J. Bogdanowicz², A. Schulze², H. H. Henrichsen⁴, F. W. Østerberg⁴, O. Hansen¹, W. Vandervorst^{2,3}, and D. H. Petersen¹

¹Department of Micro- and Nanotechnology, Technical University of Denmark, Lyngby, Denmark

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³Instituut voor Kern- en Stralingsfysika, KU Leuven, Belgium

⁴CAPRES A/S, Scion-DTU, Lyngby, Denmark

020, Understanding of and Opportunities for Electrical Characterization of Scaled Devices Using Scanning Spreading Resistance Microscopy

Kristof Paredis¹, K. Pandey^{2,1}, L. Wouters¹, U. Celano¹, O. Dixon-Luinenburg¹, T. Boehme^{2,1}, T. Hantschel¹, P. van der Heide¹, G. Pourtois¹, and W. Vandervorst^{1,2}

¹IMEC, Kapeldreef 75, B-3000 Leuven, Belgium

²Instituut voor Kern- en Stralingsfysika, KU Leuven, Celestijnenlaan 200D, B-3001 Leuven, Belgium

021, Solving Zero Sensitivity Points in Diffraction-Based Overlay Metrology

C. Messinis^{1,2}, V.T. Tenner^{1,2}, and A.J. den Boef^{1,2}

¹Department of Physics and Astronomy, and LaserLab, Vrije Universiteit, Amsterdam, The Netherlands

²Advanced Research Center for Nanolithography (ARCNL), Amsterdam, The Netherlands

022, Versatile Thickness and Composition Metrology of Graphene and Other Carbon Based Materials by SEM/EDX

Charles C Wang, Erica Chen, Christopher Lazik, and Chris Ying

Applied Materials, Inc., M/S 0203, 3100 Bowers Avenue, Santa Clara, CA

023, Low Level of –OH Analysis in SiO_x and SiCOH Dielectric Films by FTIR using Multiple Internal Reflection Technique

Yiping Yao¹, Son Nguyen², Anuja De Silva², Devika Sil², and Joyce Molinelli Acocella¹

¹IBM Systems, 2070 Route 52, Hopewell Junction, NY, 12533, USA

²IBM Semiconductor Technology Research, 257 Fuller Rd., Albany, NY, 12203, USA

024, Atom Probe Tomography Using Extreme-Ultraviolet Light

Luis Miaja-Avila¹, Ann N. Chiaramonti¹, Paul T. Blanchard¹, David R. Diercks², Brian P. Gorman², and Norman A. Sanford¹

¹National Institute of Standards and Technology, Boulder, CO, USA

²Colorado School of Mines, Golden, CO, USA

025, Group Surface Chemical Analysis of Combinatorial Film Systems

C. E. Moffitt¹, N. al-Hasan², and J. D. P. Counsell³

¹Kratos Analytical, Inc., Chestnut Ridge, NY, USA

²Dept. of Materials Sci. and Eng., University of Maryland, College Park, MD, USA

³Kratos Analytical, Ltd., Wharfside, Manchester M17 1GP, UK

026, Differential Hall Effect Metrology (DHEM) for Depth Profiling of Electrical Properties at High Resolution

Abhijeet Joshi¹, Steven W. Novak², and Bulent M. Basol¹

¹Active Layer Parametrics Inc. (ALP), Scotts Valley, CA

²SUNY Polytechnic Institute, Albany, NY

027, Size Effects on Dopant Activation in Si Fins

S. Folkersma^{1,2}, J. Bogdanowicz¹, P. Favia¹, L. Wouters¹, K. Paredis¹, A. Franquet¹, V. Spampinato¹, D. H. Petersen³, O. Hansen³, H. H. Henrichsen⁴, P. F. Nielsen⁴, L. Shiv⁴, and W. Vandervorst^{1,2}

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028, Near-Surface Sub-nm Resolution Activation Profiles in P and Sb+P Doped Ge

Pranav Ramesh¹, Krishna Saraswat¹, Abhijeet Joshi², Bulent M. Basol², Stephen P. Smith³, Larry Wang³, and Temel Buyuklimanli³

¹Stanford University, Electrical Engineering Dept., Stanford, CA

²Active Layer Parametrics (ALP), Scotts Valley, CA

³EAG Laboratories, Sunnyvale, CA

029, Understanding Structure of Doped Semiconductor Quantum Dots

Heather Renee Sully¹, Katayoun Tabatabaei², Sue Carter², Susan Kauzlarich², and Frank Bridges¹

¹University of California Santa Cruz, CA

²University of California Davis, CA

030, Latest Advancements in Nanoscale IR Spectroscopy for Failure Analysis of Electronic Devices

Anirban Roy, Qichi Hu, Honghua Yang, and Peter De Wolf

Bruker Nano Surfaces, 112 Robin Hill Road, Santa Barbara, CA

031, High Resolution Secondary Ion Mass Spectroscopy (SIMS) for Characterization

Brett Lewis, Fouzia Khanom, Alexander Lombardi, Sybren Sijbrandij, Christelle Guillermier, and John Notte

ZEISS Process Control Solutions (PCS), Carl Zeiss SMT, Inc, 1 Corporation Way, Peabody, MA

032, The Development of X-ray Metrology for Thin Film Thickness in Semiconductor Inspection

Guo-Dung Chen, Bo-Ching He, Chun-Ting Liu, Wei-En Fu, and Wen-Li Wu

Center for Measurement Standards, Industrial Technology Research Institute, Hsinchu, Taiwan

033, 3D Atomistic Mapping in Group-IV Ultrathin Silicon Germanium Superlattices

Samik Mukherjee¹, Matthias Bauer², Anis Attiaoui¹, and Oussama Moutanabbir¹

¹Department of Engineering Physics, Ecole Polytechnique de Montreal, C. P. 6079, Succ. Centre-Ville, Montreal, Quebec, Canada

²Applied Materials Inc., 974 E. Arques Avenue, Sunnyvale, CA

034, Developing a Low-cost Electric Hysteresis Measurement Device and Studying Hysteresis Effects of Thickness in PVDF

Dr. Jeffrey Carvell¹, Kyle Stewart^{1,2}, and Samantha Miller¹

¹Department of Chemistry and Physical Sciences, Marian University, Indianapolis IN

²Aerospace Engineering, San Jose State University, San Jose CA

035, Electron Reflectometry for Measuring Nanostructures on Opaque Substrate

Lawrence H. Friedman¹ and Wen-Li Wu²

¹Materials Science and Engineering Division, Materials Measurement Laboratory, NIST, Gaithersburg, MD

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036, Liquid-metal-Jet X-ray Technology for Characterization and Metrology of Nanoelectronics

Anasuya Adibhatla¹, Ulf Lundström², Björn Hansson², Julius Hållstedt²

¹55 Schuman Blvd, Naperville IL

²Excillum AB, Torshamnsgatan 35, 164 40 Kista, Sweden

037, Characterization of the Ferroelectric Phase of Encapsulated $\text{Hf}_{1-x}\text{Zr}_x\text{O}_2$ Using a Combination of Synchrotron-based Grazing Incidence XRD and Polarization Measurements

Vineetha Mukundan¹, Steven Consiglio², Kandabara Tapily², Gert Leusink², Arthur Woll³, Karsten Beckmann¹, Nathaniel Cady¹, and Alain C Diebold¹

¹SUNY Polytechnic Institute, 257 Fuller Road, Albany, NY 12203, USA

²TEL Technology Center, America, LLC, 255 Fuller Road, Suite 214, Albany, NY 12203, USA

³Cornell High Energy Synchrotron Source (CHESS), Ithaca, NY 14853, USA

038, High Precision Iron Measurement in Silicon with Whole Wafer Mapping for Contamination Control in Ultra-Pure IC Fablines

M. Wilson, A. Savtchouk, B. Schraye, C. Almeida, and J. Lagowski

Semilab SDI, 10770 N. 46th St., Ste. E700, Tampa, FL

039, Calibration of Nano- and Pico- Meter Range Displacement Measures with Static Measures

P. Luskinovich, V. Zhabotinsky, S. Rudas, A. Dikov, and A. Shavykin

Technosystems LV SIA, Latvia, Tukums

040, Micro Four-Point Probe Electrical Characterization of MoS_2

Kristoffer G. Kalhauge¹, Abhay Shivayogimath^{1,2}, David M. A. Mackenzie³, Henrik H. Henriksen⁴, Ole Hansen¹, Timothy J. Booth^{1,2}, and Dirch H. Petersen¹

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²Centre of Nanostructured Graphene (CNG), Technical University of Denmark, Denmark

³Department of Electronics and Nanoengineering, Aalto University, Micronova, Finland

⁴CAPRES A/S, Scion-DTU, Denmark

041, Analysis Lab Will Be placed on in-FAB Metrology?

Kyung Woo Lee, Joong Jung Kim, and Hongsig Kim

Samsung Electronics, 1, Samsungjeonja-ro, Hwaseong-si, Gyeonggi-do, 18448, KOREA

042, Ions and Chemistry to Fulfill Observation and Probing Specifications of sub-28nm Devices

G. Goupil¹, P. Gounet², C. Hollerith³, F. Altmann⁴, S. Brand⁴, and A. Delobbe¹

¹Orsay Physics/Tescan Orsay Holding, Fuveau, France

²ST Microelectronics, Grenoble, France

³Infineon Technologies AG, Neubiberg, Germany

⁴Fraunhofer Institute IMWS, Halle, Germany

043, Electric Field Gradient Reference Material for Scanning Probe Microscopy

J. J. Kopanski, M. Fu, and L. You

National Institute of Standards and Technology, Nanoscale Device Characterization Division,
Gaithersburg, MD

044, Direct Atomic Layer Deposition of High- κ Dielectric Films on Graphene with Assistance of Pre-H₂O Treatment

Li Zheng, Xinhong Cheng, Wen Zhou, Shaoyu Liu, Xiaobo Liu, and Yuehui Yu

State Key Laboratory of Functional Materials for Informatics, Shanghai Institute of Microsystem and
Information Technology, Chinese Academy of Sciences, Shanghai, China

045, Contribution of CL and TEM Spatial Correlation on InGaAs QW Grown on Silicon for Advance CMOS

J. Roque, S. David, N.Rochat, F. Bertin, and N. Gambacorti

Univ. Grenoble Alpes, CEA, LETI, Grenoble, France

046, Super-Resolution Single-Molecule Microscopy: a Characterization Tool for Materials

J. Alexander Liddle¹, Muzhou Wang², Stephen Stranick³, Abhishek Kumar^{1,4}, and Jeffrey Gilman³

¹Center for Nanoscale Science and Technology, National Institute of Standards and Technology

²Department of Chemical and Biological Engineering, Northwestern University

³Material Measurement Laboratory, National Institute of Standards and Technology

⁴IREAP, University of Maryland, College Park, MD